

ABSTRACT

A negative ions generating circuit design with decreasing high frequency noise includes a power indication circuit, an oscillation circuit, an amplifying circuit and a radial frequency filtering circuit. The power indicating circuit, the oscillation circuit and the amplifying circuit are used for generating negative ions. The radial frequency filtering circuit has a capacitance to be utilized for limiting the high frequency in a coil so that the high frequency will not be amplified by through the transistor of the oscillation circuit. The coil creates an inductance to limit the high frequency so as to effectively eliminate the high frequency created by the oscillation circuit.